

FIG. 1

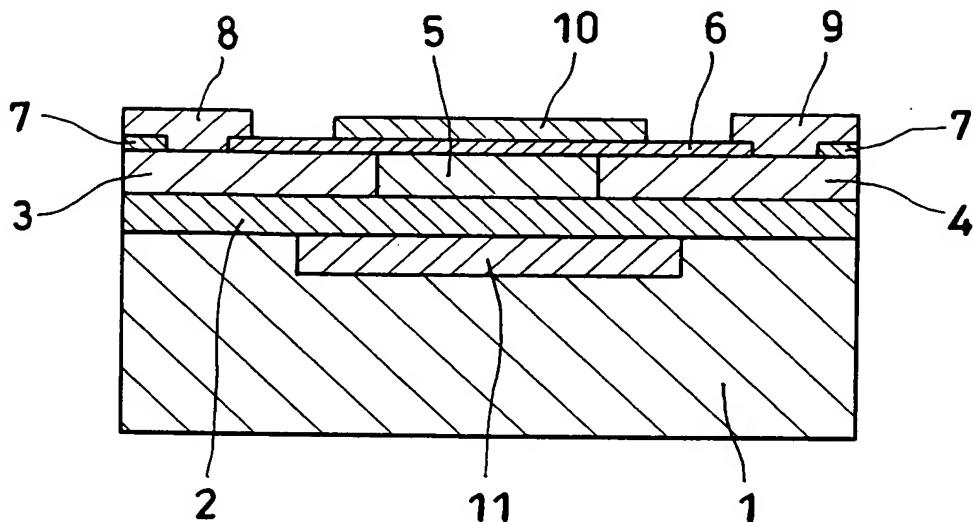
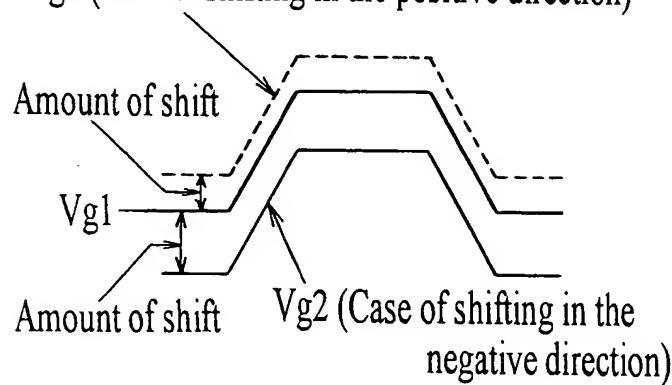


FIG. 2

(a)

$V_{g2}$  (Case of shifting in the positive direction)



(b)

$V_{g2}$  (Fast rise)

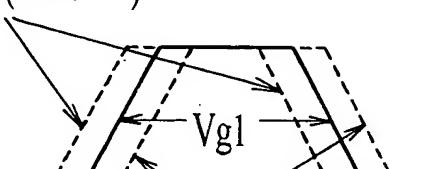
$V_{g2}$  (Slow fall)

$V_{g2}$  (Slow rise)

$V_{g2}$  (Fast fall)

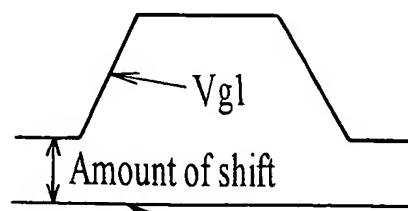
(c)

$V_{g2}$  (Advance)



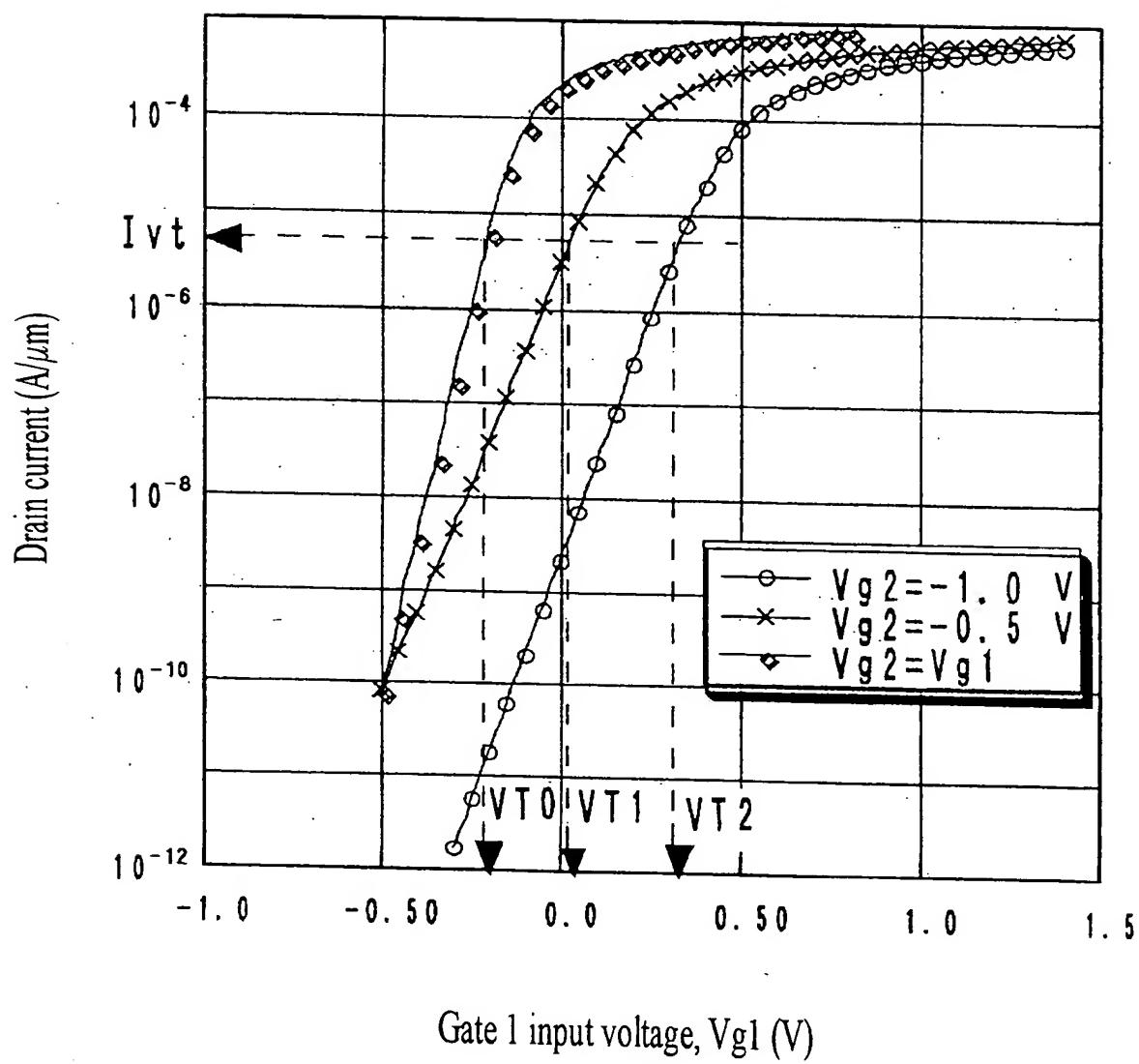
(d)

$V_{g2}$  (Constant potential)



2 / 8

FIG. 3



3 / 8

FIG. 4

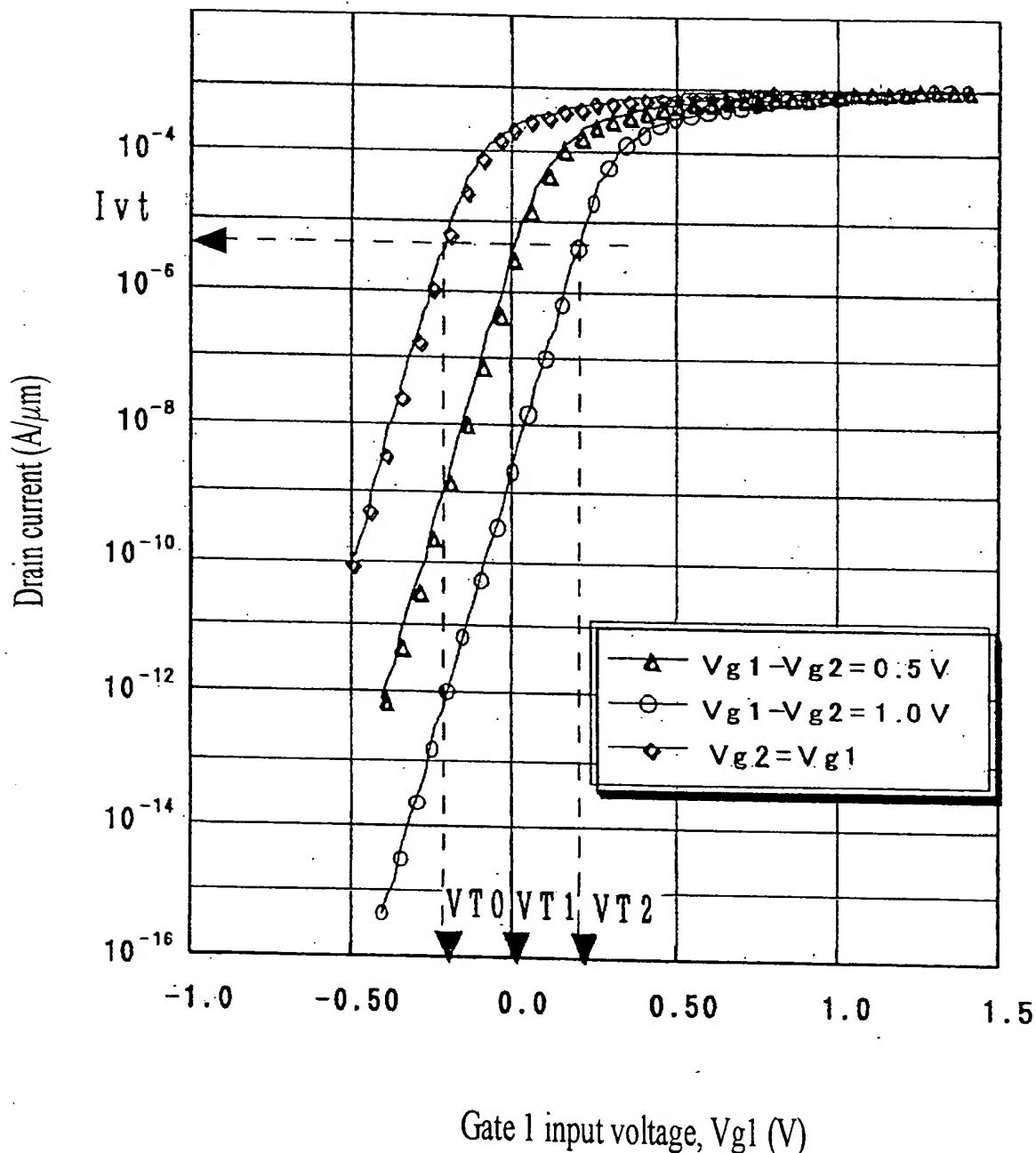
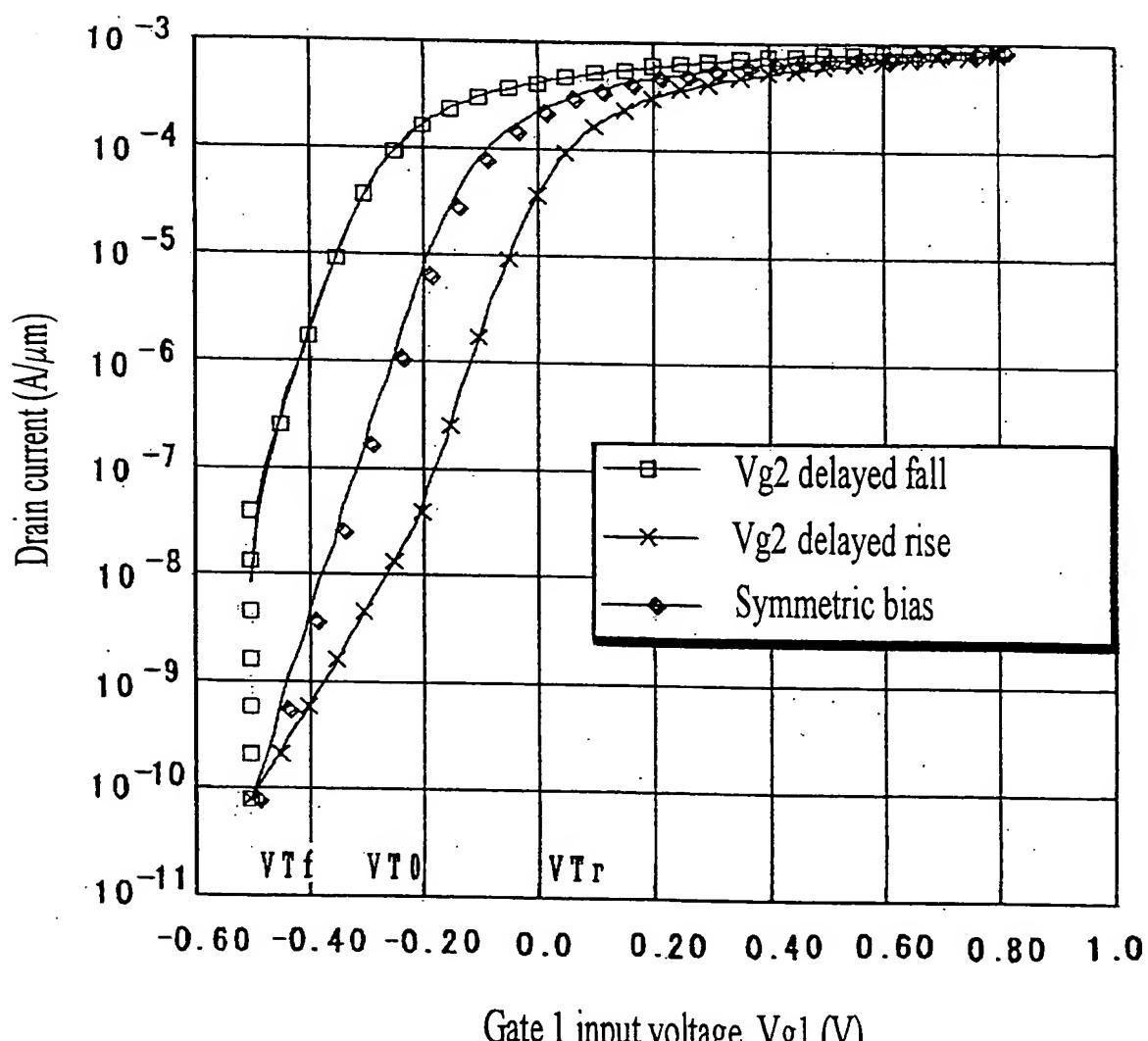


FIG. 5

Case of applying delayed input to gate 2



5 / 8

FIG. 6

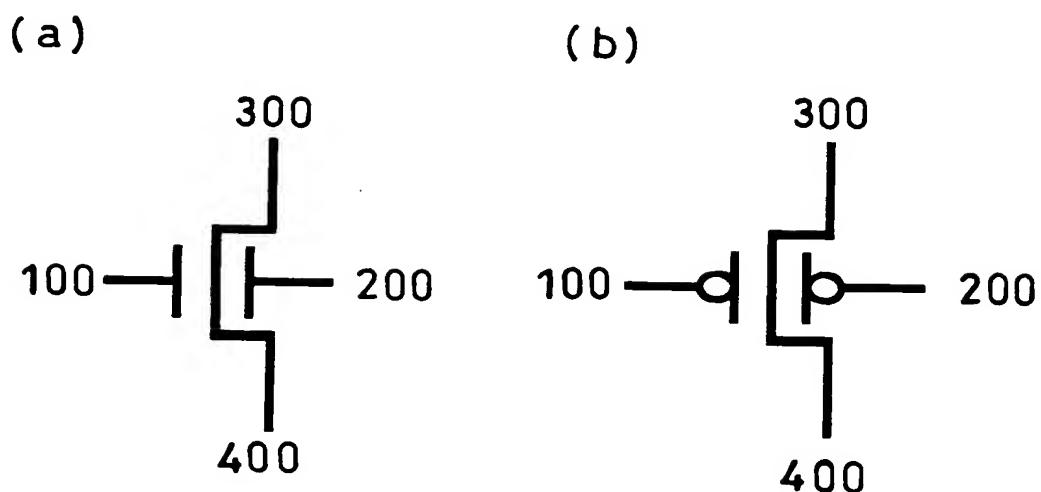
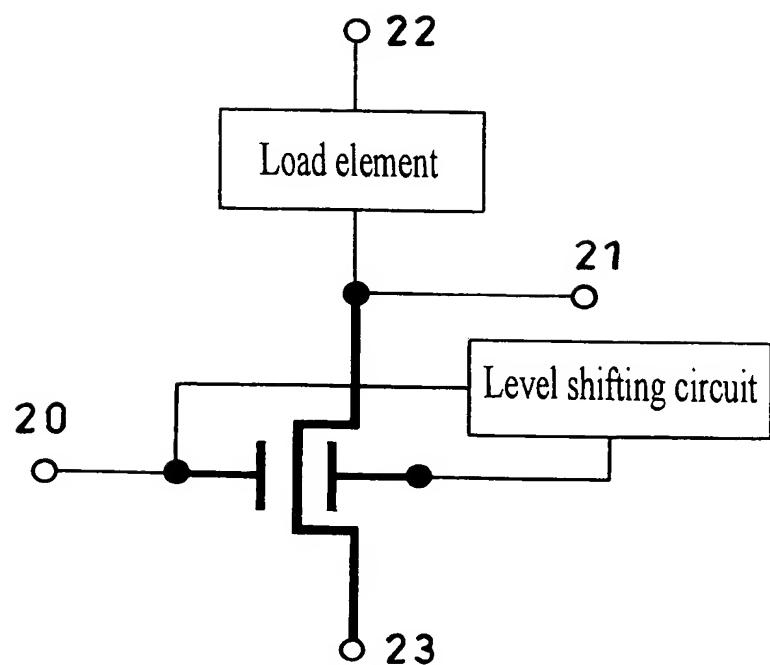


FIG. 7



6 / 8

FIG. 8

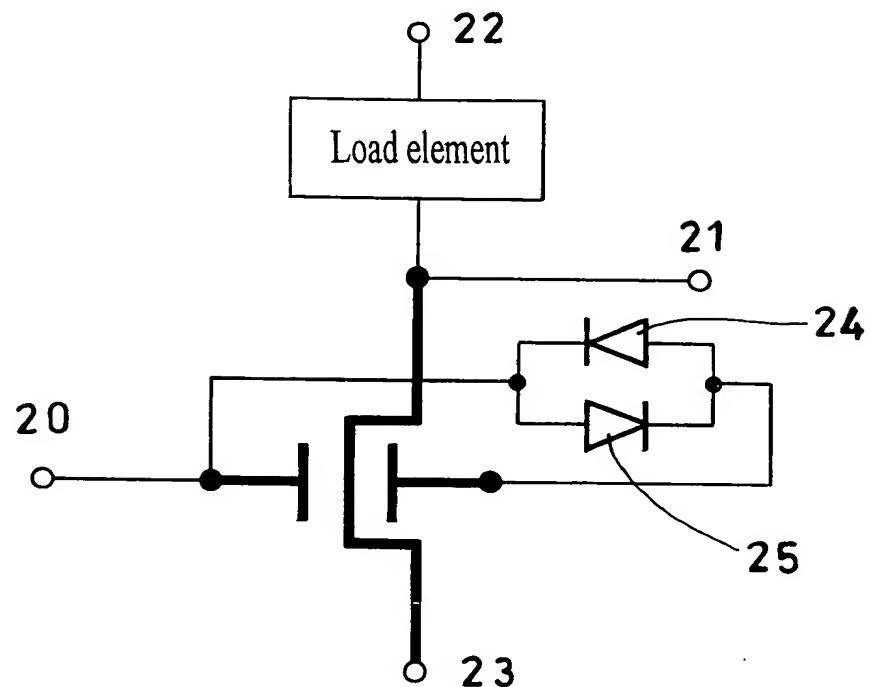
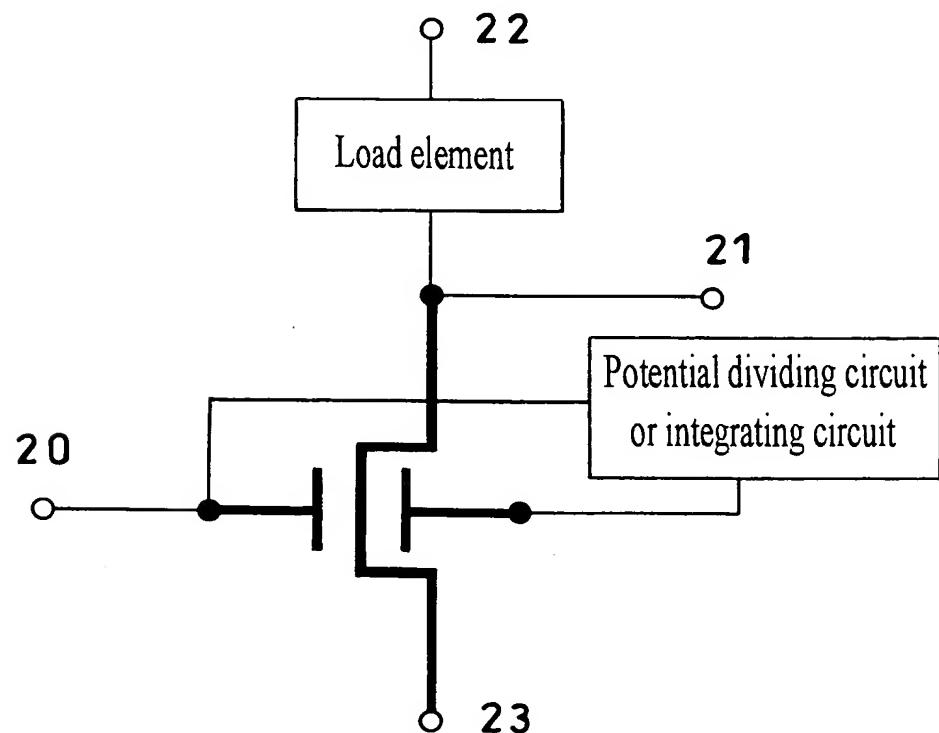


FIG. 9



7 / 8

FIG. 10

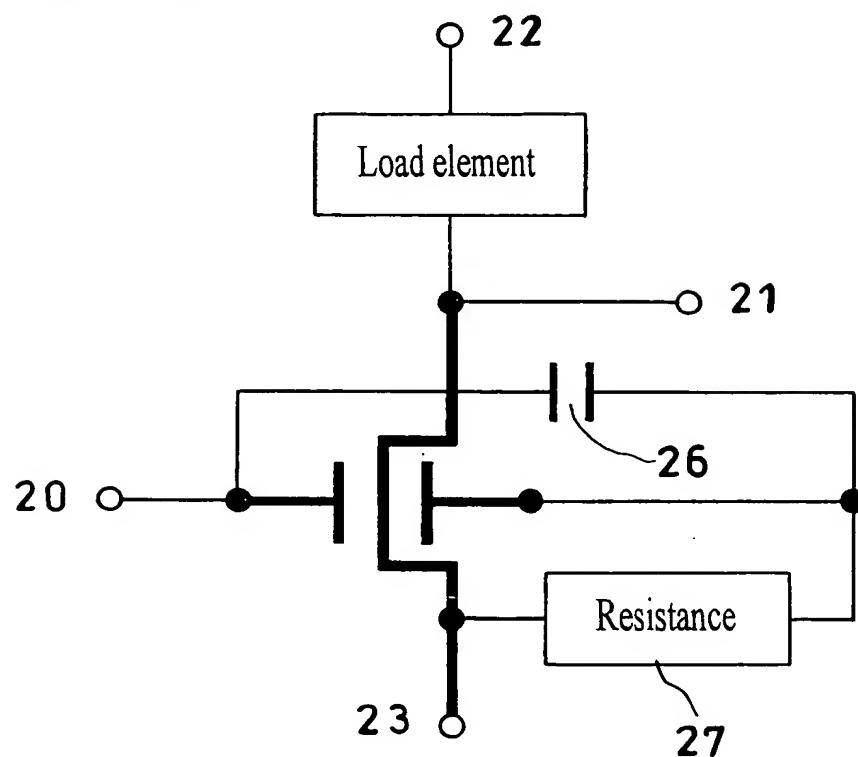
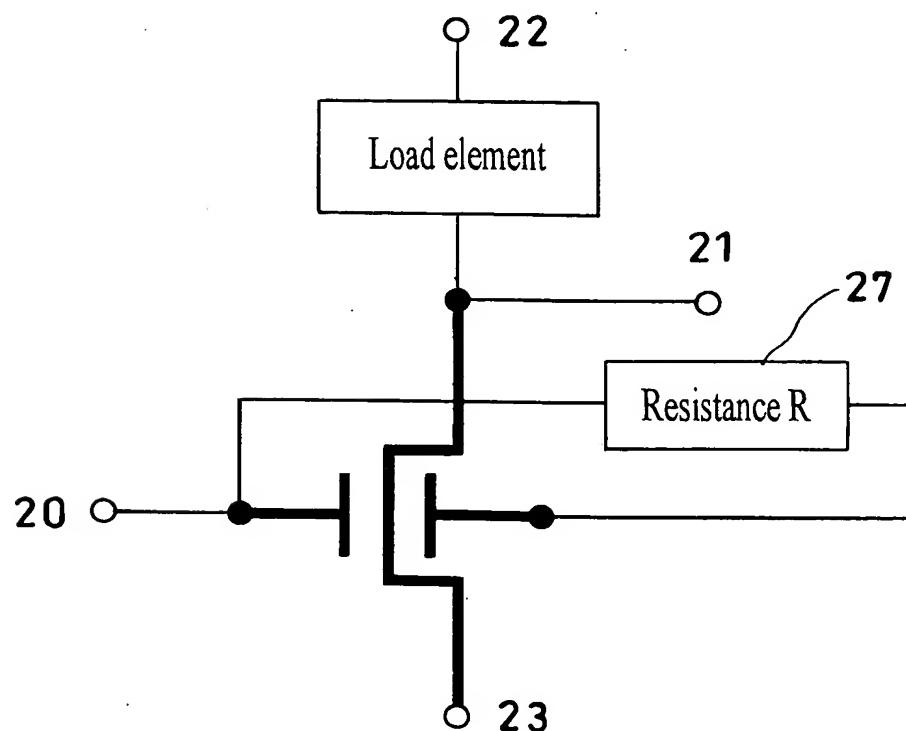


FIG. 11



8 / 8

FIG. 12

